

IN THE UNITED STATES PATENT OFFICE

Applicant: Varadarajan Srinivasan et al

Assignee: NetLogic Microsystems, Inc.

Title: "Rollover Bits For Packet Departure Time Calculator"

Ser. No.: 10/613,776 Filing Date: 07/02/2003

Examiner: Sai Ming Chan Art Unit: 2112

Docket No.: NLMI.P213 Conf. No.: 6968

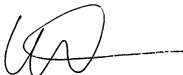
Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF DRAWINGS

Dear Sir:

Applicant submits 17 sheets of replacement drawings, consisting of Figures 1, 2, 3, 4, 5A, 5B, 6, 7, 8, 9, 10, 11, 12A, 12B, 13, 14, 15, 16, 17, and 18. If there are any questions regarding these drawings, please call the undersigned at (408) 236-6646.

Respectfully submitted,



March 26, 2007

William L Paradise III
Attorney for Applicant
Reg. No. 38,990